ICSES 2016

International Conference on Signals and Electronic Circuits

Kraków, September 5-7, 2016

Call for Papers

ABOUT THE CONFERENCE

On behalf of the Program Committee we have a great pleasure to invite you to Kraków for the 9th International Conference on Signals and Electronic Systems – ICSES 2016. The conference, which has been organized in Poland biannually (alternately with ECTTD) since 2000 is an inheritor of the National Polish Conferences on Circuit Theory and Electronics Circuits (KKTOiUE). Every previous edition brought the international scientific community together and proved to be a great opportunity for reporting, sharing and discussions on new theoretical as well as practical aspects of circuits and electronic systems. Starting from 2008 conference was technically co-sponsored by IEEE and presented papers are indexed in WoS.

SCOPE OF THE CONFERENCE

Includes but is not limited to:

Signals: Analog and digital signal processing; Speech and biomedical signal processing and coding; Multirate signal processing; A/D and D/A conversion.

Images: Image processing; Image formation, modeling and representation; Image analysis and pattern recognition; Image/video coding and transmission; Image storage, retrieval; Multimedia.

Circuits: General circuits theory; Methods of circuit analysis, synthesis, design, and optimization; Analog and digital filter design; ASICs and VLSI circuit design; Nonlinear and chaotic circuits; Switched-capacitor and switched-current circuits; Neural networks; Cellular neural networks; RF and microwave circuits.

Systems: Analog and digital electronic systems; Electronic embedded systems; VLSI and large scale systems; Data Acquisition Systems, FPGA-based systems; Multimedia systems; RF systems, Communication systems; Nonlinear, adaptive, and intelligent systems; Neural and fuzzy systems; Artificial intelligence and expert systems

Mathematical and Computational Methods: Fast algorithms and transforms; Simulation and modeling; Testing and fault analysis; Sensitivity and symbolic analysis; Multiresolution analysis and wavelets; Neural and fuzzy computing; Computer aided design.

Applications: Electronic systems, signals and image processing and recognizing in medicine, environment, communications etc.

Education: Multimedia in education; Software for teaching signals and electronic systems; Student laboratories.

IMPOTRANT DATES

June 12, 2016 Papers submission (hard deadline)
July 4, 2016 Notification of acceptance
July 18, 2016 Reduced registration fee payment
July 30, 2016 Camera-ready papers

http://www.icses2016.agh.edu.pl

PAPER SUBMISSION

Please sumbit original research work – not published elsewhere nor submitted to another conference/journal. Each paper will be reviewed by three reviewers - experts in the field. All the accepted papers will be included   in the proceedings (CD or USB). Selected papers, in a revised and expanded form will be recommended for publication in international journals.

Paper format – see the conference website

CONFERENCE FEES

Standard fee: 450 EUR

Reduced fee: 300 – 400 EUR

Standard fee includes single paper registration, admission to all sessions, coffee breaks, lunches and social events   Various reduced rates may apply for students, IEEE members provided they are paid before July 15 2016. Consult webpage for details

TRAVEL, VISAS – see the webpage

HOTEL RESERVATION, CITY TOURS

Conference fee does not include accomodation costs and each participant has to book a hotel on her/his own. However Kraków has relatively good hotel network, so hotel booking in can be easily done by any of the international booking services. Travel agency JORDAN will prepare a special offer for ICSES2016 guests. For accomodation issues (e.g. special offer for students at hostels) as well as an extra tourist program see the conference webpage.

CONFERENCE VENUE

AGH Computer Science Center

Krakow, Kawiory 21

INQUIRIES

LOCAL COMMITTEE Chair

Witold MACHOWSKI, AGH Kraków

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